

Day : Monday  
Date: 11/22/2004


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Time: 15:32:28

**Inventor Name Search Result**

Your Search was:

Last Name = CHAN

First Name = KEVIN

Application#	Patent#	Status	Date Filed	Title	Inventor Name 51
<u>60502790</u>	Not Issued	159	09/12/2003	INTEGRATED GIGABIT ETHERNET CONTROLLER	CHAN, KEVIN
<u>60451138</u>	Not Issued	159	02/28/2003	METHOD AND SYSTEM FOR AN "ALWAYS-ON INTERNET DEVICE"	CHAN, KEVIN HSUNKO
<u>60385960</u>	Not Issued	159	06/04/2002	AUTOMATIC MASSAGE CHAIR DEVICE	CHAN, KEVIN K.
<u>60365806</u>	Not Issued	159	03/21/2002	GIGABIT ETHERNET TRANSCEIVER	CHAN, KEVIN
<u>60339089</u>	Not Issued	159	12/10/2001	LOW TEMPERATURE BI-CMOS COMPATIBLE PROCESS FOR MEMS RF RESONATORS AND FILTERS	CHAN, KEVIN K.
<u>60336669</u>	Not Issued	159	12/04/2001	DYNAMIC UPSTREAM POWER EQUALIZATION	CHANG, KEVIN
<u>60202660</u>	Not Issued	159	05/08/2000	METHOD FOR MANUFACTURING DEVICE SUBSTRATE WITH METAL BACK-GATE AND STRUCTURE FORMED THEREBY	CHAN, KEVIN K.
<u>60185538</u>	Not Issued	159	02/28/2000	HIGH-SPEED TRANSMISSION SYSTEM FOR OPTICAL CHANNELS	CHAN, KEVIN
<u>10650400</u>	Not Issued	030	08/28/2003	STRAINED SILICON-CHANNEL MOSFET USING A DAMASCENE GATE PROCESS	CHAN, KEVIN K.
<u>10641691</u>	Not Issued	030	08/14/2003	METHOD AND SYSTEM FOR AN "ALWAYS-ON" INTERNET DEVICE	CHAN, KEVIN HSUNKO
<u>10632463</u>	Not Issued	020	07/31/2003	REMOTE CURRENT SENSING AND COMMUNICATION	CHANDLER, KEVIN G.

				OVER SINGLE PAIR OF POWER FEED WIRES	
<u>10616962</u>	Not Issued	093	07/11/2003	METHOD OF REDUCING POLYSILICON DEPLETION IN A POLYSILICON GATE ELECTRODE BY DEPOSITING POLYSILICON OF VARYING GRAIN SIZE	CHAN, KEVIN K.
<u>10612729</u>	Not Issued	020	07/02/2003	METHOD AND SYSTEM FOR AUTOMATIC MEDIA DEPENDENT INTERFACE RECONFIGURATION AND REPAIR	CHAN, KEVIN T.
<u>10612083</u>	Not Issued	030	07/02/2003	METHOD AND SYSTEM FOR A REDUCED EMISSIONS DIRECT DRIVE TRANSMITTER FOR UNSHIELDED TWISTED PAIR (UTP) APPLICATIONS	CHAN, KEVIN T.
<u>10612025</u>	Not Issued	030	07/02/2003	METHOD AND SYSTEM FOR SECURE AUTOMATIC MEDIA DEPENDENT INTERFACE RECONFIGURATION AND REPAIR	CHAN, KEVIN T.
<u>10605128</u>	Not Issued	071	09/10/2003	CAPACITOR AND FABRICATION METHOD USING ULTRA-HIGH VACUUM CVD OF SILICON NITRIDE	CHAN, KEVIN K.
<u>10461821</u>	Not Issued	094	06/13/2003	FULLY-DEPLETED SOI MOSFETS WITH LOW SOURCE AND DRAIN RESISTANCE AND MINIMAL OVERLAP CAPACITANCE USING A RECESSED CHANNEL DAMASCENE GATE PROCESS	CHAN, KEVIN K.
<u>10459182</u>	Not Issued	020	06/10/2003	VARIABLE BIT RATE VIDEO CODEC USING ADAPTIVE TRACKING FOR VIDEO CONFERENCING	CHAN, KEVIN
<u>10456013</u>	Not Issued	161	06/05/2003	IMAGE SENSOR MODULE	CHANG, KEVIN
<u>10456005</u>	Not Issued	030	06/06/2003	APPARATUS AND METHOD FOR PRESENCE-BASED CALL ROUTING USING COMPUTERS	CHAN, KEVIN

<u>10437930</u>	Not Issued	041	05/15/2003	SELF-ALIGNED SILICIDE PROCESS FOR SILICON SIDEWALL SOURCE AND DRAIN CONTACTS AND STRUCTURE FORMED THEREBY	CHAN, KEVIN K.
<u>10419888</u>	Not Issued	041	04/22/2003	METHOD AND STRUCTURE FOR ULTRA-LOW CONTACT RESISTANCE CMOS FORMED BY VERTICALLY SELF-ALIGNED COSI2 ON RAISED SOURCE DRAIN SI/SIGE DEVICE	CHAN, KEVIN K.
<u>10411727</u>	6762101	150	04/11/2003	DAMASCENE DOUBLE-GATE FET	CHAN, KEVIN K.
<u>10404180</u>	Not Issued	041	03/31/2003	METHOD AND APPARATUS FOR SUPPLYING POWER, AND CHANNELING ANALOG MEASUREMENT AND COMMUNICATION SIGNALS OVER SINGLE PAIR OF WIRES	CHANDLER, KEVIN G.
<u>10392831</u>	Not Issued	030	03/21/2003	PHYSICAL LAYER DEVICE HAVING AN ANALOG SERDES PASS THROUGH MODE	CHAN, KEVIN T.
<u>10386880</u>	6809005	150	03/12/2003	METHOD TO FILL DEEP TRENCH STRUCTURES WITH VOID-FREE POLYSILICON OR SILICON	CHAN, KEVIN K.
<u>10385313</u>	6801863	150	03/10/2003	METHOD FOR DETECTING THE CONNECTIVITY OF ELECTRICAL CONDUCTORS DURING AUTOMATED TEST USING LONGITUDINAL BALANCE MEASUREMENTS	CHANDLER, KEVIN G.
<u>10374866</u>	Not Issued	041	02/25/2003	SHALLOW TRENCH ISOLATION STRUCTURE FOR STRAINED SI ON SIGE	CHAN, KEVIN KOK
<u>10372778</u>	Not Issued	092	02/26/2003	PROGRAMMABLE GAIN AMPLIFIER WITH GLITCH MINIMIZATION	CHAN, KEVIN T.
<u>10334176</u>	Not Issued	030	12/30/2002	LOW TEMPERATURE FUSION BONDING WITH HIGH SURFACE ENERGY USING A WET CHEMICAL TREATMENT	CHAN, KEVIN K.

<u>10328258</u>	Not Issued	071	12/23/2002	NITRIDE-ENCAPSULATED FET (NNCFET)	CHAN, KEVIN K.
<u>10162974</u>	Not Issued	041	06/05/2002	FULLY INTEGRATED ETHERNET TRANSMITTER ARCHITECTURE WITH INTERPOLATING FILTERING	CHAN, KEVIN T.
<u>10156782</u>	<u>6690072</u>	150	05/24/2002	METHOD AND STRUCTURE FOR ULTRA-LOW CONTACT RESISTANCE CMOS FORMED BY VERTICALLY SELF- ALIGNED COSI2 ON RAISED SOURCE DRAIN SI/SIGE DEVICE	CHAN, KEVIN K.
<u>10124967</u>	<u>6594304</u>	150	04/18/2002	ADAPTIVELY CONFIGURABLE CLASS- A/CLASS-B TRANSMIT DAC FOR TRANSCEIVER EMISSION AND POWER CONSUMPTION CONTROL	CHAN, KEVIN T.
<u>10119799</u>	<u>6580132</u>	150	04/10/2002	DAMASCENE DOUBLE-GATE FET	CHAN, KEVIN K.
<u>10102365</u>	Not Issued	094	03/20/2002	SELF-ALIGNED NANOTUBE FIELD EFFECT TRANSISTOR AND METHOD OF FABRICATING SAME	CHAN, KEVIN K.
<u>10095233</u>	Not Issued	121	03/11/2002	REDUCTION OF AGGREGATE EMI EMISSIONS OF MULTIPLE TRANSMITTERS	CHAN, KEVIN T.
<u>10093789</u>	<u>6821864</u>	150	03/07/2002	NOVEL METHOD TO ACHIEVE INCREASED TRENCH DEPTH, INDEPENDENT OF CD AS DEFINED BY LITHOGRAPHY	CHAN, KEVIN K.
<u>10091099</u>	<u>6690742</u>	150	03/05/2002	ADAPTIVELY CONFIGURABLE CLASS- A/CLASS-B TRANSMIT DAC FOR TRANSCEIVER EMISSION AND POWER CONSUMPTION CONTROL	CHAN, KEVIN T.
<u>10087239</u>	<u>6744831</u>	150	03/01/2002	ADAPTIVE ELECTRONIC TRANSMISSION SIGNAL CANCELLATION APPARATUS FOR FULL DUPLEX COMMUNICATION	CHAN, KEVIN T.
<u>09684358</u>	Not	161	10/06/2000	RATCHET WRENCH HAVING	CHANG, KEVIN

	Issued			TWO ROTATABLE JAWS	
<a href="#">09596725</a>	<a href="#">6262464</a>	150	06/19/2000	ENCAPSULATED MEMS BAND-PASS FILTER FOR INTEGRATED CIRCUITS	CHAN, KEVIN K.
<a href="#">09569306</a>	Not Issued	071	05/11/2000	SELF-ALIGNED SILICONE PROCESS FOR LOW RESISTIVITY CONTACTS TO THIN FILM SILICON-ON-INSULATOR MOSFETS	CHAN, KEVIN KOK
<a href="#">09568520</a>	<a href="#">6389077</a>	150	05/09/2000	ADAPTIVELY CONFIGURABLE CLASS-A/CLASS-B TRANSMIT DAC FOR TRANSCEIVER EMISSION AND POWER CONSUMPTION CONTROL	CHAN, KEVIN T.
<a href="#">09550990</a>	Not Issued	094	04/17/2000	SCALABLE MOS FIELD EFFECT TRANSISTOR	CHAN, KEVIN KOK
<a href="#">09524623</a>	<a href="#">6246135</a>	150	03/13/2000	CENTRIFUGAL SWITCH DEVICE FOR INDUCTION MOTOR	CHANG, KEVIN C. Y.
<a href="#">09518521</a>	Not Issued	161	03/04/2000	TOOTHBRUSH WITH MOLDABLE HANDLE	CHANG, KEVIN
<a href="#">09512721</a>	<a href="#">6218315</a>	150	02/24/2000	HTO (HIGH TEMPERATURE OXIDE) DEPOSITION FOR CAPACITOR DIELECTRICS	CHAN, KEVIN K.
<a href="#">09501226</a>	<a href="#">6339002</a>	150	02/10/2000	METHOD UTILIZING CMP TO FABRICATE DOUBLE GATE MOSFETS WITH CONDUCTIVE SIDEWALL CONTACTS	CHAN, KEVIN K.
<a href="#">09501225</a>	<a href="#">6333247</a>	150	02/10/2000	TWO-STEP MOSFET GATE FORMATION FOR HIGH-DENSITY DEVICES	CHAN, KEVIN K.
<a href="#">09463360</a>	<a href="#">6358545</a>	150	05/15/2000	BEVERAGE-PRODUCING PACKAGES	CHANDLER, KEVIN PAUL

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